

CLAIMS:

1. Integrated circuit comprising a plurality of modules (M, S), and a network (N) arranged for transferring messages between said modules (M, S), wherein a message issued by a first module (M) comprises first information indicative for a location of an addressed module within the network, and second information indicative for a location within the
5 addressed module (S),
the integrated circuit comprising
at least one address translation means (AT) for arranging the first and the second information as a single address,
wherein said address translation means (AT) is adapted to determine which
10 module is addressed based on said single address, and
wherein the selected location of the addressed module (S) is determined based on said single address.
2. Integrated circuit according to claim 1, further comprising:
15 at least one interface means (ANIP, PNIP) associated to one of the modules (M, S) for managing the communication between said associated module (M, S) and the network (N),
wherein one of said address translation means (AT) is arranged in one of said interface means (ANIP, PNIP).
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3. Integrated circuit according to claim 2, wherein
wherein said address translation means (AT) is arranged in said interface means (ANIP, PNIP) associated to said first module (M).
- 25 4. Integrated circuit according to claim 2 or 3, wherein
said address translation means (AT) comprises an address mapping table (AMT).
5. Integrated circuit according to claim 4, wherein

said address mapping table (AMT) contains fields for every channel of a connection, for network interface ports (ANIP, PNIP) of a connection, and for local addresses in addressed modules (S).

- 5 6. Method for exchanging messages in an integrated circuit comprising a plurality of modules (M, S), the messages between the modules (M, S) being exchanged via a network (N), wherein a message issued by a module (M) comprises first information indicative for a location of an addressed module (S) within the network, and second information indicative for a location within the addressed module (S),
- 10 the method including the steps of:
- arranging the first and the second information as a single address,
 - determining which module is addressed based on said single address, and
 - determining the selected location of the addressed module (S) based on said single address.